Preparation of silicon-based nanowires and the thermochemistry of the process

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Abstract
The present paper is part of a general investigation of the thermodynamic properties of metal-semiconductor systems involved in the nanowires growth. Here, we will show that many aspects of the growth mechanisms, such as the surface curvature of the solid phase (in the nano or micrometer range), in equilibrium with the liquid phase are extremely important. We calculate and construct all binary Au-Si phase diagrams corresponding to nanosystems. By choosing the S(V)LS process rather than the VLS process, we succeeded in answering not only the origin of the driving force needed to trigger off the precipitation of solid nanowire but also the thorny question of the size limit of nanowires in relation with our specific growth conditions.

Keywords:
Silicon nanowires, VLS mechanism, thermodynamics, phase diagrams, nanosystems.

1 INTRODUCTION
The synthesis of semiconductor nanowires has recently attracted much attention for their potential applications in electronic, photonic and biological areas [1]. Hence, many efforts have been devoted to developing different approaches for synthesizing a variety of semiconductor nanowires. However, Silicon nanowires are much desirable because of the central function that silicon plays in the current semiconductor processing technology. The most extensively synthesis method used is based on the vapour phase technique with Au particles as mediating solvent. The vapour synthesis method used is based on the vapour phase semiconductor processing technology. The most extensively synthesis method used is based on the vapour phase technique with Au particles as mediating solvent. The vapour phase of the desired material can be generated through various sources: either the decomposition of gaseous precursors such as (SiH4, SiCl4, Si2H6) or the thermal vaporization of condensed phases. In these techniques, the generally accepted mechanisms of the nanowire growth are referred to the vapour-liquid-solid (VLS) process when the precursor is gaseous or Solid-Liquid-Solid (SLS) process when the starting material to be evaporated is solid. Whatever the process used, the solid nanowire is formed by precipitation from a supersaturated droplet alloy (Au-constituent of the growing material). The diameters of the nanowires are dictated by the size of the alloy droplet considered as a template. In the VLS process in which silane gas acts as Si feedstock, it is easy to understand the origin of supersaturation of gold droplet as the silicon chemical potential in gaseous phase is much higher relative to silicon substrate. This contrasts with the SLS process. Actually, in the SLS process, silicon nanowires are grown by heating the metal-coated silicon substrate (wafer) at high temperature in an atmosphere of argon and hydrogen. The only source of silicon is “apparently” the solid silicon wafer.

Based on the bulk Au-Si phase diagram, many researchers [2-4] postulated that the growth of nanowires by the SLS mechanism occurred through silicon diffusion from the substrate to the gold-silicon melt formed on the surface at high temperatures. With time, more silicon from the substrate diffuses into the droplet, making it supersaturated with silicon and then the coexisting pure silicon phase precipitates and crystallizes as nanowires. The same interpretation has been used to explain the growth of nanowires on other metals: Ni, Pt, Pd, Au, Fe [5-7]. However, no explanation was given to justify the supersaturation.

From a thermodynamic point of view, it is easy to accept the saturation of gold with silicon. However, once the saturation is obtained, the chemical potential of silicon in solid, liquid alloy (metal-Si) and gaseous phases is the same, and there is no thermodynamic (energetic) reason for the silicon of the wafer to supersaturate the binary liquid phase.

The true mechanism had never been proposed, neither species involved nor have the most important reaction pathways for nanowire growth been investigated. Systematic data which would be helpful for elucidation of the growth mechanism of nanowires, as well as information concerning the binary Au-Si diagram for the catalytic nanosystem, are practically unavailable in the literature. A better understanding of the nanowire growth process in the vapor phase is needed to elucidate the growth mechanism and to be able to rationally control their compositions, sizes, crystal structures, and growth directions.

Thus, the purpose of the present work is to discuss, from a thermodynamic point of view coupled with experimental work, the growth of nanowires by the SLS process and to point out the conditions in which nanowires can be obtained.

2 EXPERIMENTAL PART
Successful synthetic strategy has been developed to obtain nanowires. Single-side polished n-type silicon wafers (111) (Siltronix) were used as substrates for subsequent gold deposition. The wafers were first cleaved and then cleaned using a standard RCA process. The substrates were placed into a vacuum chamber, where 5 nm thick layer of gold was thermally evaporated onto their polished surface. The synthesis chamber consists of a horizontal tube (quartz or alumina for high temperatures) heated by a three-zone furnace, with gas introduced into one end and pumped out
the other. For the SLS process, the experiments were carried out under a dynamic gas atmosphere (3% H2 in nitrogen: 99.995 purity) at a flow rate of 50 cm3/min. The gold coated wafers were annealed up to 1100°C, with the heating rate of 10°C/min.

The as-synthesized nanowires are characterized and analyzed by field emission scanning electron microscopy Ultra 55 ZEISS (FE-SEM) and transmission electron microscope (TEM) (JEOL 2010 at 200 kV).

3 RESULTS AND DISCUSSION

3.1 Phase diagrams

To understand the origin of silicon-based nanowires growth, it is necessary to back off a bit and consider the interaction between Au and Si. Therefore, we begin by examining the Au-Si phase diagram by taking into account the possible equilibria between a solid particle and a liquid droplet. The radius of the liquid droplet is not necessarily identical to that of the solid. In the usual Au-Si phase diagrams both the solid and liquid phases are either macroscopic or nanoscopic with the same shape and size [8, 9]. The macroscopic phase diagram in which both phases are larger than 100 nm does not correspond to the system involved in nanowire growth. In fact, at the beginning of the S(V)LS process, the solid is macroscopic (wafer substrate), that is, in equilibrium with a liquid in nano-meter range; then, after precipitation of silicon and the start of the nanowire growth, we may consider a new system in which a nanoscopic solid (nanowire) is in equilibrium with a nanoscopic liquid (alloy droplet). Therefore, it should be emphasized that attention must be paid to all these systems where the liquid phase is in the nanometer range whereas the solid phase is either macroscopic or nanoscopic in size. Moreover, if the solid phase is in the nanometer range, we have to distinguish between a nanometric solid whose shape is spherical and the nanowires whose shape is cylindrical.

The Au-Si phase diagrams are calculated by using the CALPHAD method (CALculation of PHAse Diagrams). This is based on finding the minimum total Gibbs energy of the system at constant temperature and pressure, yielding the composition and the amount of phases in equilibrium. The liquid solutions are modelled using the Redlich-Kister polynomial.

In figure 1 the calculated phase diagrams of typical systems involved during the growth process of nanowires are plotted. For the sake of simplicity, only three metastable systems are represented and compared with the best known stable system, in which the liquid and the solid are both in the macroscopic range. When the liquid is nanometric, it is supposed to be spherical with 5 nm radius. When the solid is nanometric, it is supposed to consist of spherical nanocrystals of 5 nm radius or cylindrical nanowires of 5 nm radius.

The phase diagrams in figure 1 are all of the same shape. Gold forms a simple and deep eutectic system with silicon. However, the eutectic temperature and the eutectic composition depend on the solid/liquid system considered. It must be pointed out that the calculations are made under the implicit hypothesis that the surface tension does not depend on the particle size. Such an assumption has been proved valid [10] for nanoparticles whose radius exceeds 4 nm. For smaller particles, surface tension decreases and tends towards zero when the diameter of the particle tends towards zero.

The most important new information provided in figure 1 is that the silicon solubility depends not only on the size of the liquid droplet but also on the shape and size of the solid in equilibrium with that liquid. This calculation will be extended to illustrate an important point, namely, the melting behaviour of gold particles and the nature of the solid surface (substrate) on which the nucleation of nanowires has occurred. This kind of analysis is in progress; however, it is beyond the scope of the present study.

With the variety of cases encountered in this study, we are now in a position to treat a liquid alloy (Au-Si) of any size in equilibrium with any silicon-based solid (nano or macroscopic size).

These diagrams also afford an opportunity for verifying descriptions of other related synthesis methods (CVD, GS-MBE…) by the VLS process.

Valuable studies on the growth of nanowires by the VLS mechanism have been reported by many researchers [11] who refer to the macroscopic Au-Si system. However, the difference in the eutectic temperature of macroscopic system and the one corresponding to small gold particles in equilibrium with the solid substrate shows that knowledge of the bulk system alone is not sufficient to permit the evaluation of systems in nanometric range.

Thermodynamically, however, these diagrams show significant differences in liquidus temperatures and not fundamental differences in the nature of the components. Thus, the condensed phases present in the system show that there is no convenient driving force coming from the wafer to justify any supersaturation of the gold nanodroplet with silicon of the substrate. Hence, particular attention will be paid on Si present in the gas phase.

3.2 SLS process: Phase gas analysis

Si wafers coated thick Au film were heated at 1000°C for 1 h. figure 2 (A) shows the SEM image of the Au deposited on Si wafer, heated for 10 min. At this stage the Au film on the Si wafer breaks up and nanoparticles with diameters in the range 10–100 nm were formed on the Si surface. These nanoparticles act as the nucleation site for the growth of Si-based nanowires figure 2 B.

In the SLS mechanism, Au, Si and Ar (with or without H2) are the main elements introduced in the synthesis chamber. Another element to be considered is oxygen which arises either from leakage, from degassing of Al2O3 tube or simply from SiO2 which may be native or even intentionally grown by thermal deposition on the substrate.
A thermochemical analysis was made of the reduction and volatilization which occur in the Si/O system. Hence, the silicon species present in the gas phase may be Si, Si$_2$, Si$_3$ in equilibrium with the solid silicon, or SiO in equilibrium with the Si-SiO$_2$ mixture.

Figure 3 shows log $P_{SiO}$ vs $P_{O_2}$ plots of the data. The vertical lines in the figure give the oxygen pressures for the oxidation reaction:

$$ Si(s) + O_2 \rightleftharpoons SiO_2(s) \quad [A] $$

The most important feature in figure 3 is the SiO (g) pressure peak at the interface Si(s)-SiO$_2$(s). The main equilibrium to be considered is thus the following one:

$$ Si(s) + SiO_2(s) \rightleftharpoons 2 SiO(g) \quad [B] $$

$$ R T \ln \left( \frac{P_{SiO}}{P^o} \right) = - \Delta_B G^o + R T \ln \left( \alpha_{Si} \times \alpha_{SiO_2} \right) $$

$$ \Delta_B G^o = 718.614.749 - 532.537.892 T + 24.469.069 T T \ln T $$

$$ + 4.994.867.82 \times 10^{-4} T^2 + 187.917.772 \times 10^{-9} T^3 - 923 266.15 T^{-1} $$

The coefficients are extracted from [12].

The standard state for SiO$_2$ is the solid cristobalite, which is the stable form of silica under the temperature range of interest. As seen above, the standard state for Si is the pure solid silicon $<Si$, diam>; the standard pressure for gaseous species (O$_2$ and SiO) is $P^o = 10^5$ Pa.

If the wafer ($\alpha_{Si} = 1$) is in equilibrium with the solid cristobalite ($\alpha_{SiO_2} = 1$), the calculated partial pressure of SiO is 4.05 Pa at 1100°C. When the equilibrium is reached, the chemical potential of silicon is the same in every phase and no more driving force exists to justify the nanowire growth experimentally observed. The explanation of this process has to be researched in the presence of metastable equilibria developed in the system.

The only source of metastability comes from the fact that SiO$_2$ on the wafer is amorphous silica rather than crystallized under the form of stable cristobalite, as it can be easily shown by X-ray diffraction on the silica layers. Actually, all deposited and thermally grown oxides in semiconductor process are amorphous.

Thus, the activity of the amorphous silica deposited on the silicon wafer would be:

$$ a_{SiO_2, am} = P_{SiO_2, am} \times P_{SiO_2, crist} > 1 $$

The driving force of the silicon nanowire growth $\Delta G$ is the chemical potential of silicon in the droplet in equilibrium with the gas phase and that of silicon wafer:

$$ \Delta_G = \mu_{Si, supersaturation} - \mu_{Si, gas} - \mu_{Si, diam} $$

This difference brings the driving force needed to supersaturate the gold droplets with Si. Gaseous SiO above the wafer, stable with respect to the Si/SiO$_2$ (amorphous) mixture, is thus metastable with respect to the Si/SiO$_2$ (cristobalite) mixture, and the reaction [B] will naturally proceed towards the decomposition of SiO. Without gold droplets, Si and SiO$_2$, resulting from the disproportion reaction 2 SiO → SiO$_2$ + Si, will germinate on the wafer. The gold droplets acting as a catalyst, SiO will decompose on the surface of the droplet and silicon will dissolve until the liquid-gas equilibrium is reached. When the liquid is in equilibrium with the gaseous phase but supersaturated in silicon with respect to the wafer, the precipitation of silicon nanowires will be observed. Amorphous SiO$_2$ synthesized by the disproportion reaction will deposit on the nanowire and on the wafer.
\[ \Delta G = -2 R T \ln \left( \frac{P_{\text{SiO}}}{P_{\text{SiO}}^\text{Si diam}} / P_{\text{SiO}}^\text{Si diam/SiO2 am} \right) \]

The Gibbs energy of the transformation of Si (diamond) into Si (nanowire) via SiO is related to the radius of nanowire by the following equation:

\[ \Delta G \propto V / r_{\text{min}} \]  

(5)

\[ r = \left[ R T \ln \left( \frac{P_{\text{SiO nanoam}}}{P_{\text{SiO diam}}} \right) \right]^{-1} = 4.5 \text{ nm} \]

The nanowire growth energy increases when \( r \) decreases, cannot be higher than the available energy \( \Delta G \), a result which implies the existence of a minimum radius \( r_{\text{min}} \). Thus, what we labelled the first generation of nanowires have their radius higher or equal to \( r_{\text{min}} = 4.5 \text{ nm} \) at 1100°C as confirmed by SEM analysis (figure 2 B and C).

The question then arises as to how nanowires (first-generation) are affected by their thermal surrounding. In other words: How do nanowires behave upon the constraints imposed by the system? The relationship between activities of solids and pressures of gases resulting from the reaction (A) is expressed by:

\[ K(T) = \left( \frac{P_{\text{SiO}}}{P_{\text{SiO}}^\text{Si diam}} \right)^2 = \exp \left( -\frac{\Delta G^*}{R T} \right) \]  

(4)

\[ K(T) \] is the equilibrium constant closely related to the Gibbs energy \( \Delta G^* \) of the reaction (A) and depends only on the temperature. It will be recalled that the superscript (°) refers to all component of the reaction being in their standard states that is Si, SiO2 and SiO under the diamond, cristobalite and gaseous form at 0.1 MPa pressure, respectively.

Once the first-generation of nanowires are grown, a new phase which is metastable silicon nanowire, is formed. Amorphous SiO2 is still present as a metastable phase and the SiO pressure developed in the system is no more \( P_1 = 4.562 \text{ Pa} \) but a new pressure \( P_2 = 5.212 \text{ Pa} \) which is generated by the equilibrium \( \text{Si, nanowire} \rightarrow \text{SiO}_2 \text{ amorphous} \). While \( K(T) \) has a fixed value, the equilibrium pressures of the individual components do not. Thus there is an infinite number of sets of pressures of reactants and products which satisfy equation [4] and which correspond to equilibrium states of the system. \( P_2 \) the SiO pressure at the Si nanowire/SiO2 amorphous equilibrium and \( P_0 \) the SiO pressure at the Si diamond/SiO2 cristobalite equilibrium are computed from \( K \).

It should be noted that such pressure change of SiO from \( P_1 \) to \( P_2 \) occurs according the vapourisation reaction of Si nanowires and SiO2. The vapourisation of the first-generation of nanowires supplies enough energy to proceed with the growth of the next generation of nanowires. The free energy imposes a new size limit of nanowires. The corresponding minimum radius of the second-generation of nanowires is calculated by the same relation (5):

\[ r = \sigma_{\text{Si}} V_{\text{Si}} / \Delta G = 2.39 \text{ nm} \]

The minimum value of \( r \) decreases when \( P_{\text{SiO}} \) increases, thus, nanowires characterized by \( r < 4.78 \text{ nm} \) must be generated by a SiO pressure \( P_2 > P_1 \).

Taking into account the lack of precision of the numerical values, it is possible to say that the minimum diameter of the second generation nanowires is half of the diameter of the first-generation nanowires. The second generation nanowires are rarer than those of first-generation owing to the difficulty in obtaining liquid nanodrops with a radius of the order of 2.5 nm. The great majority of droplets have a radius higher than 2.5 nm and it is hard to imagine the formation of a nanowire whose diameter would be much lower than the droplet size. However, the process involving the vaporization of the first-generation of nanowires will tend to cause the radius of nanowires to decrease whereby the SiO pressure is high enough to allow the growth of the next generation of nanowires whose size limit has been estimated to be near 2.5 nm.

The subsequent generation (third, fourth...) of silicon nanowires growth proceeds in the same way as before in the cycle considered previously.

The calculation is confirmed by TEM observations (Figure 2D). The process is considered as a permanent repetition of the cycles by growth and vaporization of the wires. By this process, there is theoretically no minimum size for the nanowires.

4 SUMMARY

Our experimental results are in full agreement with our thermodynamics predictions. In detail, the S(V)LS is used to fabricate a composite Si/SiO2 nanowires. The present paper evaluates the case of Si-Au, but the principles should be sufficiently general to hold for other binary alloys as well.

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5 REFERENCES